

I. Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

A. Listing of Claims

Claims 1-4 (Cancelled).

5. (Currently Amended) A display device according to claim 11, further comprising:

a first memory for keeping the display data therein; and

a second memory for keeping the blanking data therein, wherein:

the control circuit reads the display data from the first memory at timing synchronized with the first clock signal, outputs the display data to the data driver, reads from the second memory the blanking data corresponding to spaces where said second clock signal is not created ~~from the second memory at timing generating signal just before the timing which is synchronized with the first clock signal and at which the second clock signal is not created~~, and outputs the blanking data to the data driver.

6. (Previously Presented) A display device according to claim 11, wherein a period of the first clock signal and a period of the second clock signal are synchronized with a scanning period for the scan driver to select pixels of at least one of the rows of pixels.

7. (Currently Amended) A display device according to claim 11, wherein:

the scan driver sequentially selects one row of pixels in response to the second clock signal and selects the pixels twice for each row at a period of one frame in response to the scanning start signal;

the scan driver selects n rows of pixels ~~at timing generating signal just before the timing~~

~~at which the second clock signal is not created~~ corresponding to spaces where said second clock signal is not created;

the data driver supplies the tone voltage corresponding to the display data to the pixels of one row in response to the first clock signal; and

the data driver supplies the tone voltage corresponding to the blanking data to the pixels of n rows.

8. (Currently Amended) A display device according to claim 11, wherein the control circuit outputs to the scan driver a first scanning enable signal to invalidate selection of the pixels by the scan driver ~~at timing-generating signal just before the timing at which the second clock signal is not created~~ corresponding to spaces where said second clock signal is not created and a second scanning enable signal to validate selection of the pixels by the scan driver ~~at timing-generating signal just before the timing at which the second clock signal is not created~~ corresponding to spaces where said second clock signal is not created.

Claims 9-10 (Cancelled).

11. (Currently Amended) A display device, comprising:

- a pixels array including a plurality of pixels arranged in a form of a matrix;
- a data driver for supplying a tone voltage corresponding to display data to the pixels;
- a scan driver for selecting pixels of at least one row to which the tone voltage is to be supplied; and
- a control circuit for controlling the data driver and the scan driver, wherein:
 - the control circuit outputs a first clock signal and the display data to the data driver;
 - the control circuit outputs to the scan driver a second clock signal, the second clock

signal not being created every n ($n > 2$) signal creation thereof and outputs a scanning start signal generated a plurality of times during one frame period; and

the control circuit outputs to the data driver blanking data other than the display data in place of the display data ~~at timing at which the second clock signal is created immediately before the timing at which the second clock signal is not created~~ that corresponds to spaces where said second clock signal is not created.

12. (Currently Amended) A display device according to claim 11, wherein:

the scan driver selects the pixels of one row in response to the second clock signal and the scanning start signal during a period of time from a horizontal scanning period starting at timing at which the second clock signal is created immediately before the timing ~~at which the second clock signal is not created~~ that corresponds to spaces where said second clock signal is not created to a horizontal scanning period starting at timing at which the second clock signal is not created; and

the scan driver selects the pixels of n rows during one horizontal scanning period at which the second clock signal is created immediately before the timing at which the second clock signal is not created.

13. (Currently Amended) A display device according to claim 12, wherein:

the data driver supplies to the pixels the tone signal corresponding to the display data in response to the first clock signal during a horizontal scanning period ~~starting at timing at which the second clock signal is created immediately before the timing at which the second clock signal is not created~~ corresponding to spaces where said second clock signal is not created; and

the data driver supplies to the pixels the tone signal corresponding to the blanking data

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during a horizontal scanning period ~~starting at timing at which the second clock signal is not created~~ corresponding to spaces where said second clock signal is not created.

Claims 14-20 (Cancelled)